

# WEST Search History

DATE: Friday, September 29, 2006

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L15	(L10 or L12 or L13 or L14) and vertical.clm.	178
<input type="checkbox"/>	L14	257/401.ccls.	1378
<input type="checkbox"/>	L13	365/188.ccls.	111
<input type="checkbox"/>	L12	365/182.ccls.	627
<input type="checkbox"/>	L11	L10 and SRAM	53
<input type="checkbox"/>	L10	257/365.ccls.	502
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L9	L7 and vertical	2388
<input type="checkbox"/>	L8	L7 and (vertical with (MOSFET or MISFET or IGFET or transistor or gate))	0
<input type="checkbox"/>	L7	L5 and gate	12750
<input type="checkbox"/>	L6	L5 and gate and (vertical with (MOSFET or MISFET IGFET or transistor or gate))	0
<input type="checkbox"/>	L5	((L3 or L4) and (@pd<=20030730 or @rlad<=20030730 or @ad<=20030730)) not L2	16352
<input type="checkbox"/>	L4	365/\$.ccls. and SRAM	11216
<input type="checkbox"/>	L3	257/\$.ccls. and SRAM	10990
<input type="checkbox"/>	L2	L1 and (vertical with (MOSFET or MISFET or transistor or gate))	2150
<input type="checkbox"/>	L1	SRAM and (@pd<=20030730 or @rlad<=20030730 or @ad<=20030730) and vertical	11384

END OF SEARCH HISTORY